DETAILED ACTION

1. Claims 1-2, 4-20 & 22-36 are pending in the application.

2. Claims 3 & 21 have been canceled.

EXAMINER'S AMENDMENT

3. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Chad J. Billings on 03/12/2010.

The Claims have been amended as follows:

- In Claim 1, line 20 **replace** "...packet data..."

With

"...packet data; wherein when the control means calculates the clock frequency deviation between the data sending device and the data receiving device and generates the read timing of the received packet data, the control means calculates a target value by dividing the integration result of the amount of the received packet data written in the storage means in a predetermined period from a beginning of receiving of the packet data, by an average receiving rate of the received packet data in an integration time, calculates a measurement value by dividing the integration result of the amount of the received packet data written in the storage means after the predetermined period, by the average receiving rate of the received

packet data in the integration time, and sets a difference between the target value

and the measurement value as the clock frequency deviation…"

- **Cancel** <u>Claim 3</u>

- <u>In Claim 20, line 20</u> **replace** "...timing of the received packet data..."

With

"… timing of the received packet data; wherein when a clock frequency deviation

between the data sending device and the data receiving device is calculated and the

read timing of the received packet data is generated, a target value is calculated by

dividing the integration result of the amount of the received packet data written in the

storage means in a predetermined period from a beginning of receiving of the packet

data, by the average receiving rate of the received packet data in the integration

time, a measurement value is calculated by dividing the integration result of the

amount of the received packet data written in the storage means after the

predetermined period, by the average receiving rate of the received packet data in

the integration time, and a difference between the target value and the measurement

value is set as the clock frequency deviation…"

- **Cancel** <u>Claim 21</u>

Allowable Subject Matter

4. Claims 1-2, 4-20 & 22-36 and re-numbered as claims 1-to-34 respectively are

   allowed.

5. Claims 1-2, 4-20 & 22-36 and re-numbered as claims 1-to-34 respectively are

   allowable over the prior art of record because the cited references do not contain

the specified limitation of a data receiving device for receiving packet data

through a transmission path and outputting the packet data in accordance with

time information added beforehand to the packet data, the data receiving device

comprising: a storage means for storing the received packet data; and a control

means for separating the time information added to the received packet data and

reading the packet data from the storage means in accordance with the

separated time information; wherein when the Control means generates a read

timing, the control means calculates a clock frequency deviation between a data

sending device and the data receiving device in accordance with an integration

result of an amount of the received packet data temporarily stored in the storage

means and a measurement result of the integration time, and adds an offset

amount based on the deviation to the read timing of the received packet data,

thereby generating the read timing of the received packet data; wherein when the

control means calculates the clock frequency deviation between the data sending

device and the data receiving device and generates the read timing of the

received packet data, the control means calculates a target value by dividing the

integration result of the amount of the received packet data written in the storage

means in a predetermined period from a beginning of receiving of the packet

data, by an average receiving rate of the received packet data in an integration

time, calculates a measurement value by dividing the integration result of the

amount of the received packet data written in the storage means after the

predetermined period, by the average receiving rate of the received packet data

in the integration time, and sets a difference between the target value and the measurement value as the clock frequency deviation.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to SUDHANSHU C. PATHAK whose telephone number is (571)272-5509. The examiner can normally be reached on 9am-5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chieh M. Fan can be reached on 571-272-3042. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Sudhanshu C Pathak/
Primary Examiner, Art Unit 2611